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APPLICANTS Sadami Takeoka, Osaka, JAPAN; Takahiro Ichinomiya, Osaka, JAPAN; Akira Motohara, Hyogo, JAPAN;				
** CONTINUING DATA ***** This application is a CON of 08/803,145 02/19/1997 PAT 6,282,506				
** FOREIGN APPLICATIONS ***** JAPAN 8-032152 02/20/1996				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 05/15/2001				
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged _____ Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 26	TOTAL CLAIMS 11
				INDEPENDENT CLAIMS 10
ADDRESS McDERMOTT, WILL & EMERY 600 13th Street, N.W. Washington ,DC 20005-3096				
TITLE METHOD OF DESIGNING SEMICONDUCTOR INTEGRATED CIRCUIT UTILIZING A SCAN TEST FUNCTION				
FILING FEE RECEIVED 1270	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	